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134. Sastanak IEEE u Novom Sadu / 134th IEEE Meeting in Novi Sad **Obaveštenje** / **Announcement**

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u <u>sredu, 24. 12. 2014.</u> u Amfiteatru A1 Fakulteta tehničkih nauka u Novom Sadu, sa početkom u **18:15 h**, održati

On <u>Monday, December 24, 2014</u>, in the **Amphitheater A1** of the Faculty of Tech. Sciences Novi Sad at <u>6:15 pm</u> will deliver

P R E D A V A N J E L E C T U R E

Scaling up Transaction Processing on Hardware Islands

Abstract: Transaction processing is one of the most important applications using relational database systems and a multibillion dollar market. They are typically used for mission critical enterprise systems and run on the highest performing servers of the day. Up until recently, such servers had uniform core-to-core communication latencies. Now with the emergence of multisocket multicores, for the first time we have Islands, i.e., groups of cores that communicate fast among themselves and slower with other groups. The goal of this presentation is to quantify the impact of hardware Islands on the performance of transaction processing systems and propose ways to overcome their negative impact. In the first part we present a detailed analysis on the impact of non-uniform hardware topology on the performance of different transaction processing architectures. We compare different database deployment strategies ranging from a single shared-everything instance to fine-grained shared-nothing configurations. In the second part of the presentation, we will present the dynamic transaction processing system that combines the best characteristics of different configurations to provide good performance for various workloads on hardware islands. The main design principles in this system are elimination of unneccessary communcation between islands, dynamic workload monitoring and fast repartitioning whenever workload changes.

Odsek za Primenjene računarske nauke i informatiku

